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| <b>INFORMATION DISCLOSURE<br/>STATEMENT</b> | Atty Docket: | 00GR35154360  |
|   | Serial No.:  | 10/082,816    |
|   | Applicant:   | COFLER ET AL. |
|   | Filing Date: | 02/25/2002    |
|   | Group:       |               |

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| 14   | AR | Toyohiko Yoshida et al., "A Strategy for Avoiding pipeline Interlock Delays in a Microprocessor", published by LSI Research and Development Laboratory on September 17, 1990; Publication No: XP 000201400; pages 14-19; ISBN: 0-8186-2079-X. |
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|--|-----------------------------------|
| EXAMINER:<br><i>Deny Fan</i>   | DATE CONSIDERED:<br><i>8/2/04</i> |
| *EXAMINER Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. |                                   |